

S69	56	(bit\$1 near3 permut\$6) and (active near bit)	US-PGPUB; USPAT	OR	OFF	2007/06/05 18:38
S70	688	(bit\$1 near3 permut\$6) and byte\$1	US-PGPUB; USPAT	OR	OFF	2007/06/06 09:49
S71	277	(tree with scheme) and decoder	US-PGPUB; USPAT	OR	OFF	2007/06/06 09:50
S72	0	S71 and S70	US-PGPUB; USPAT	OR	OFF	2007/06/06 09:49
S73	6	(tree with scheme) and decoder and (bit\$1 near3 permut\$6)	US-PGPUB; USPAT	OR	OFF	2007/06/06 09:50
S74	171	(tree with scheme) and decoder and binary	US-PGPUB; USPAT	OR	OFF	2007/06/06 09:54
S75	271	(tree with (scheme or structure)) and decoder and (address near signal)	US-PGPUB; USPAT	OR	OFF	2007/06/06 09:59
S76	630	(tree with (scheme or structure)) and (decoder with circuit)	US-PGPUB; USPAT	OR	OFF	2007/06/06 09:59
S77	4	(tree with (scheme or structure)) and (decoder with (digital near circuit))	US-PGPUB; USPAT	OR	OFF	2007/06/06 10:00
S78	630	(tree with (scheme or structure)) and (decoder with circuit)	US-PGPUB; USPAT	OR	OFF	2007/06/06 10:06
S79	549	(decoder with circuit) and (byte near input)	US-PGPUB; USPAT	OR	OFF	2007/06/06 10:04
S80	8	(tree with (scheme or structure)) and (decoder with circuit).ti.	US-PGPUB; USPAT	OR	OFF	2007/06/06 10:09
S81	344	(decoder with circuit).ti.	US-PGPUB; USPAT	OR	OFF	2007/06/06 10:20
S82	1	(decoder with circuit).ti. and subdivide	US-PGPUB; USPAT	OR	OFF	2007/06/06 10:21
S83	23	(decoder with circuit).ti. and (input near subdivide or subdivision or divide)	US-PGPUB; USPAT	OR	OFF	2007/06/06 10:27
S84	79	(decoder with circuit).ti. and half	US-PGPUB; USPAT	OR	OFF	2007/06/06 10:34
S85	16	(decoder with circuit).ti. and tree	US-PGPUB; USPAT	OR	OFF	2007/06/06 10:34
S86	5	"2003010919".did.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2007/10/18 11:30
S87	1597	(substitution or s) adj box \$2	US-PGPUB; USPAT	OR	OFF	2008/06/02 15:28
S88	48	hardware with S87	US-PGPUB; USPAT	OR	OFF	2008/06/02 15:31

S89	1597	(substitution or s) adj box \$2	US-PGPUB; USPAT	OR	OFF	2008/06/02 19:26
S90	15	string\$1 with S89	US-PGPUB; USPAT	OR	OFF	2008/06/02 19:26
S91	76	circuit with S89	US-PGPUB; USPAT	OR	OFF	2008/06/02 19:29
S92	2142	active adj bit	US-PGPUB; USPAT	OR	OFF	2008/06/02 19:33
S93	1	S92 and S91	US-PGPUB; USPAT	OR	OFF	2008/06/02 19:33
S94	201	S92 and S89	US-PGPUB; USPAT	OR	OFF	2008/06/02 19:33
S95	15	decod\$3 and S91	US-PGPUB; USPAT	OR	OFF	2008/06/02 19:36
S96	40	permut\$\$6 and S91	US-PGPUB; USPAT	OR	OFF	2008/06/02 19:44
S97	40	permut\$6 and S91	US-PGPUB; USPAT	OR	OFF	2008/06/02 19:44
S98	1	("2005/0002523").URPN.	USPAT	OR	OFF	2008/06/02 20:36
S99	0	("2005/0169463").URPN.	USPAT	OR	OFF	2008/06/02 20:39
S100	0	("2005/0169463").URPN.	USPAT	OR	OFF	2008/06/02 20:39
S101	24	S89 with ((sum\$1 near2 product\$1) or (binary))	USPAT	OR	OFF	2008/06/02 20:40
S102	1	S101 and S91	USPAT	OR	OFF	2008/06/02 20:41
S103	13448	((sum\$1 near2 product\$1) or (binary near2 decision))	USPAT	OR	OFF	2008/06/02 20:41
S104	3	S103 and S91	USPAT	OR	OFF	2008/06/02 20:41
S105	195	(decoder adj circuit).ti.	US-PGPUB; USPAT	OR	OFF	2008/06/03 17:36
S106	160	S105 and gate\$1	US-PGPUB; USPAT	OR	OFF	2008/06/03 17:36

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